

DEBASHISH NANDI

PhD Scholar (PMRF), IIT Kanpur



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EDUCATION

- 2022-** **Ph.D.: Microelectronics And VLSI**
Current *Indian Institute of Technology Kanpur - Kanpur, UP*
Coursework Details: Annexed.
CGPA: **9.86/10** (ongoing)
- 2013 -2017** **B.Tech.: Electronics and Communication Engineering**
National Institute of Technology, Durgapur - Durgapur, WB
CGPA: **9.47/10** (in absolute scale); Rank: Fifth (Department), Tenth (University)
- 2013** **Higher Secondary: P-C-M-B**
Jitpur High School - Alipurduar, WB
Grade: **92.8%**; Board: WBCHSC; Rank: **9** (in State)
- 2011** **Secondary: General**
Railway Higher Secondary School - Alipurduar, WB
Grade: **90.25%**; Board: WBBSE; Rank: within Top 100 (in State)

WORK EXPERIENCE

- 2017 -2021** **Asst. Manager (E&C)**
Larsen and Toubro Limited, Faridabad, HR, India
- Key Responsibilities:**
1. End to end design integration of Railway Telecom subsystems namely SDH-PDH, WAN, GSM-R, Master Clock System, EPABX, DTS, PAS, PIDS for DFCCIL, Indian Railways mainline (new & modification), Metro Projects in India and abroad, as part of EDRC (Engineering Design and Research Centre).
 2. Integration of Telecom systems with Signalling Works, E&M works, Track works, Train Management System, Electronic Interlocking.
 3. Planning, Cost Controlling of Signalling and Telecom works for DFCCIL CTP-14 (Rewari-Dadri) Project valued INR 3799 Cr.
 4. Interface Management for S&T works with stakeholders (mainly GOI, JICA) of the WDFC CTP-14 Project.
- Key Skills Acquired:** Telecom System Design, Project Management, RAMS, System Integration

INTERNSHIPS AND TRAININGS

- 2017** **Graduate Engineer Trainee**
Competency Development Centre, Kanchipuram, TN
- Undergone 1-month comprehensive training in Railways construction including design and EPC contract management. Trained in P-way, OHE, PSI, Signalling and Telecom system Design and Construction. Secured 3rd rank in the final evaluation among 24 students.
- Key Skills Acquired:** Basics of Railways construction, EPC Contract management, FIDIC.
- 2016** **Research Intern**
Indian Institute of Technology Guwahati, AS
- Worked under guidance of **Dr. Gaurav Trivedi** (Associate Professor, EE) on Atomistic Simulations in 'Virtual Nanolab ATK tool' for 2D materials like Silicene, Germanene, Stanene etc. Investigated Bandgap generation in these materials by studying their bandstructures, introducing various defects namely Stone Wales, Single vacancy etc. Also studied their terminal characteristics by simulating various device structures.

Key Skills Acquired: 2D simulations on ATK tools like Virtual Nanolab, Research paper writing, Conference presentation.

2015

Trainee Intern

Hewlett-Packard Education Services, Kolkata, WB

Undergone 1-month hands-on training in Embedded Systems and Robotics - Basics and Advanced. Have worked on PCB design, PCB integration and made a line following robot as part of the end-project. Secured 2nd rank in the final evaluation among 30+ students.

Key Skills Acquired: Embedded Systems basics, MMI, PCB design.

CO-CURRICULAR ACTIVITIES

1. 2013-2014: Member of National Service Scheme in NIT Durgapur and taught underprivileged students from nearby villages below class X.
2. 2011: Organized and managed science exhibition in Jitpur High School, Alipurduar, WB.
3. Interests: Teaching, Literature, Badminton, Football, Table Tennis. Snooker.

AWARDS/HONORS

1. 2022: Received Prime Minister's Research Fellowship to pursue PhD in the broad area of Electrical Engineering.
2. 2018-2021: Rated Top Performer in every financial year performance review in Larsen & Toubro.
3. 2013: Chief Minister's (WB) award of excellence in study along with a laptop for securing State Rank-9 in Higher Secondary Examination.
4. 2013: Divisional Railway Manager's (APDJ) award of excellence in study with a tablet.
5. 2005: State sponsored scholarship (*Vritti*) of excellence in completion of Primary education.

LANGUAGES

Bengali, Hindi: Native Proficiency
English: Full Professional Proficiency
Assamese: Elementary Proficiency

PUBLICATIONS

1. G. Kumar, M. Singh, **D Nandi**, G Trivedi, Bandgap generation in 2D materials, *IEEE conference Devices for Integrated Circuit (DevIC)*, 2017, doi: 10.1109/DEVIC.2017.8074011

Enclosed: Annexure-1 (PhD Coursework Details & TA experience)

ANNEXURE-1
PhD Coursework Details (IIT Kanpur)

S. No.	Subject Code	Subject Name	Semester	Grade	Project/Term Paper
1.	EE-681A	<i>Compact Modelling</i> Instructor: Prof. Yogesh Singh Chauhan	2021-2022 (II)	A	1. Parameter Extraction of BSIM4 2. Development of Verilog A Code for BULK MOSFET
2.	EE-613A	<i>High Frequency Analog Circuit Design</i> Instructor: Dr. Imon Mondal	2021-2022 (II)	A	1. Design of LDO Reg. 2. Design of Two-Stage Fully Differential Miller Compensated OPAMP
3.	EE-619A	<i>VLSI System Design</i> Instructor: Dr. Rik Dey	2021-2022 (II)	A	Design of Multiple Digital Combinational Circuit Blocks and Verilog Implementation in Behavioral, Structural, Data Flow Levels
4.	EE-698N	<i>Introduction to Flexible Electronics</i> Instructor: Prof. Baquer Mazhari	2021-2022 (II)	A	Role of Carbon Nanotube, applications and its advancement in Flexible Electronics.
5.	EE-614A	<i>Solid State Devices I</i> Instructor: Prof. Alope Dutta	2022-2023 (I)	B+	N/A
6.	EE-616A	<i>Semiconductor Device Modelling</i> Instructor: Dr. Rik Dey	2022-2023 (I)	A*	N/A
7.	EE-698F	<i>RF Microelectronics</i> Instructor: Prof. Yogesh Singh Chauhan	2022-2023 (I)	A*	Design of Low Noise Amplifier in 2-4GHz Frequency band with GaAs HEMT.
8.	EE-648A	<i>RF Microelectronics</i> Instructor: Dr. Raghavendra Chowdhury	2022-2023 (II)	#	1. Analysis of Theory of Small Reflections for CCITLs. 2. Analysis of Async. Coupled Lines in Inhomogeneous Medium.
9.	EE-656A	<i>RF Microelectronics</i> Instructor: Prof. Nishchal Kumar Verma	2022-2023 (II)	#	Implementation of Self-Optimal Clustering Algorithm.

Currently undergoing

Grading Scheme:

A*	A	B+	B	C+	C	D+	D	E	F	I
10	10	9	8	7	6	5	4	0	0	0

Teaching Assistantship:

- EE210A: Microelectronics-I (May'22 to Jun'22)
TA Supervisor: Dr. Rik Dey
- EE380A: Electrical Engineering Lab-I (Jul'22 to Nov'22)
TA Supervisor: Prof. Baquer Mazhari
- NPTEL NOC EE-62: Basic Electronics (Jan'23 to Apr'23)
Instructor: Prof. Mahesh B Patil (IIT Bombay) :24 Hrs.
- MATLAB Workshop at CSJMU (Kanpur University) :30Hrs.